We are working on reducing power consumption and making the system even more compact, with the goal being a practicable compact video recorder for Super Hi-Vision cameras. We are also developing a removable solid-state memory package.

Outline

We are conducting research into a compact video recorder for a Super Hi-Vision camera to enable highly mobile program production. We present a compact video recorder that compresses a Super Hi-Vision camera signal and records it in parallel to solid-state memory.

Features

• Reduction in size of video recorder
  We have developed a compact signal processing board that compresses the signal from the image sensor and have made the device more compact by reducing the memory chips that are necessary for recording.

• High-speed parallel recording using solid-state memory
  We have developed a parallel write algorithm that shortens the waiting time for writing to solid-state memory. This makes it possible to increase recording speed to twice that of conventional systems.

In the works

We are working on reducing power consumption and making the system even more compact, with the goal being a practicable compact video recorder for Super Hi-Vision cameras. We are also developing a removable solid-state memory package.

• This research is being conducted in cooperation with Tokyo Electron Device Limited.

Recording and playback of Super Hi-Vision video by compact video recorder